FIG. 1A

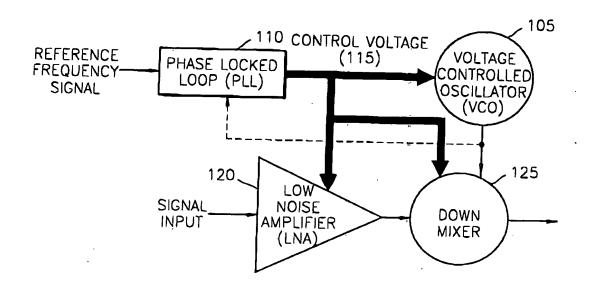
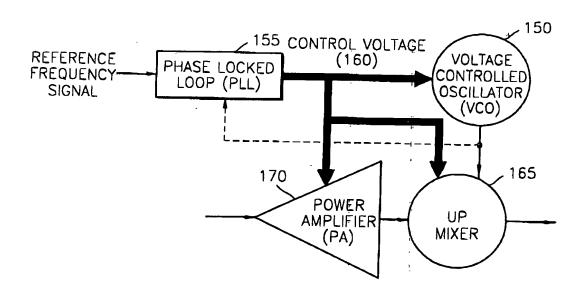
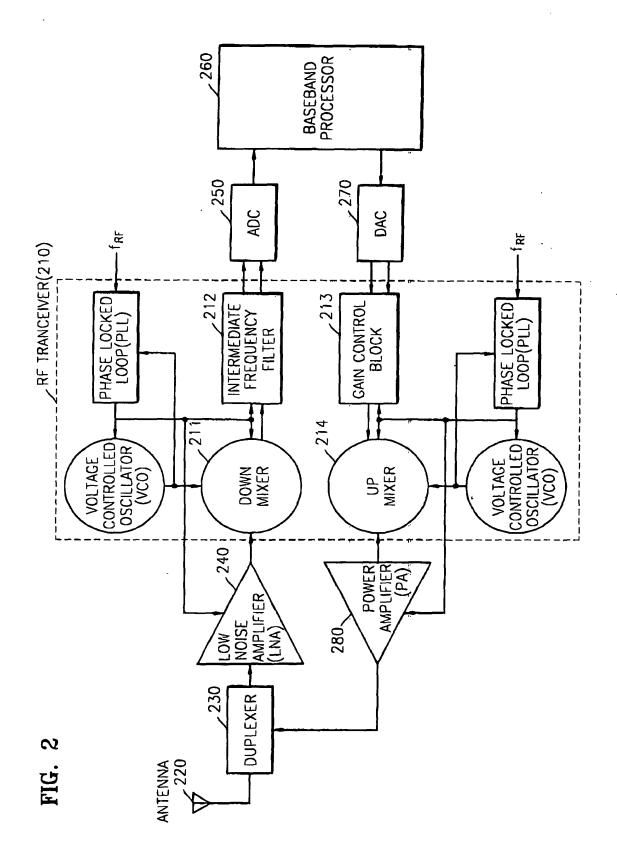


FIG. 1B





 $f_{out} = \frac{M}{N} \cdot f_{RF}$

FIG. 4

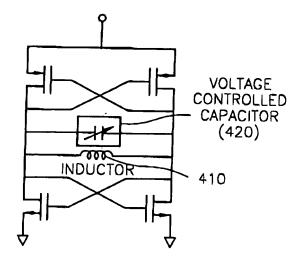


FIG. 5

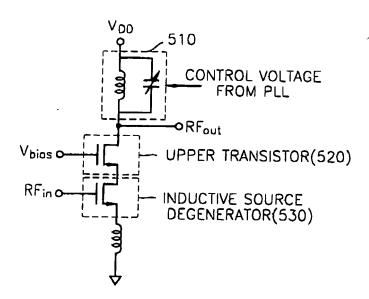


FIG. 6

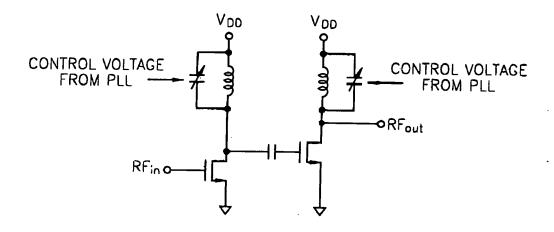


FIG. 7

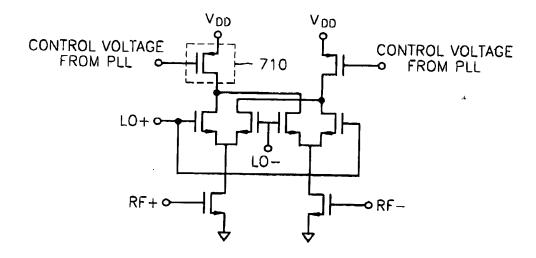


FIG. 8

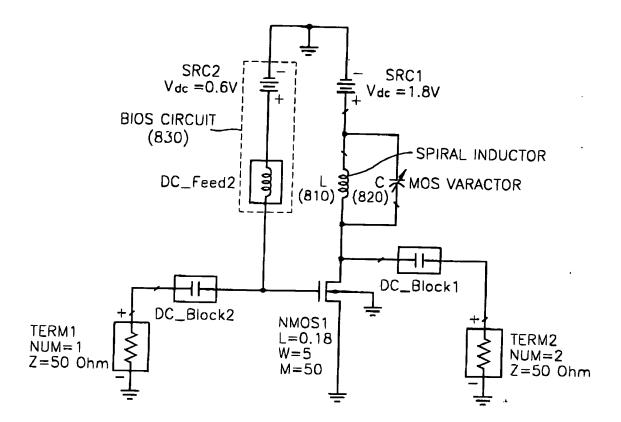


FIG. 9

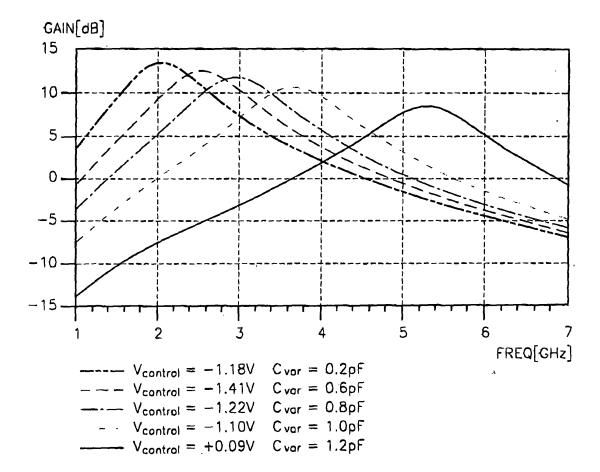


FIG. 10 START RECEIVE SIGNAL S1010 GENERATE PLL CONTROL VOLTAGE -S1020 AMPLIFY RECEIVED SIGNAL WHILE -51030 SUPPRESSING NOISE SIGNAL CONVERT AMPLIFIED SIGNAL INTO LOW-FREQUENCY BAND SIGNAL -S1040 **END** FIG. 11 START -51110 RECEIVE SIGNAL GENERATE PLL CONTROL VOLTAGE - \$1120 CONVERT RECEIVED SIGNAL INTO -S1130 HIGH-FREQUENCY BAND SIGNAL AMPLIFY POWER OF CONVERTED -S1140 SIGNAL END